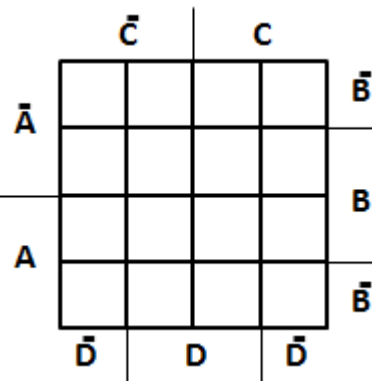




5. Simplifique via mapa de Veitch-Karnaugh a seguinte tabela verdade e construa o circuito mínimo:

A	B	C	D	S
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	0
1	0	0	0	1
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	1
1	1	0	1	0
1	1	1	0	1
1	1	1	1	1

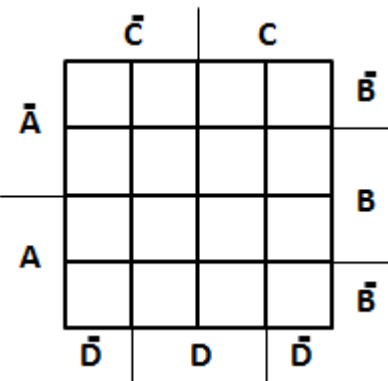


A B C D

Four vertical lines for drawing the logic circuit.

6. Simplifique via mapa de Veitch-Karnaugh a seguinte tabela verdade e construa o circuito mínimo:

A	B	C	D	S
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	1
1	0	0	1	1
1	0	1	0	-
1	0	1	1	-
1	1	0	0	-
1	1	0	1	-
1	1	1	0	-
1	1	1	1	-



A B C D

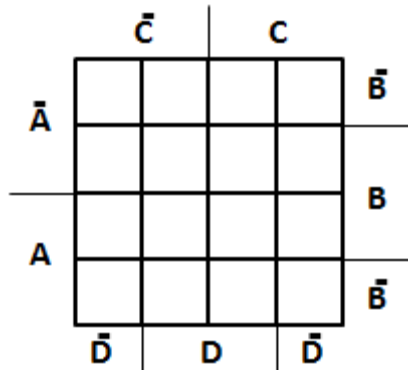
Four vertical lines for drawing the logic circuit.

Obs: - significa don't care!

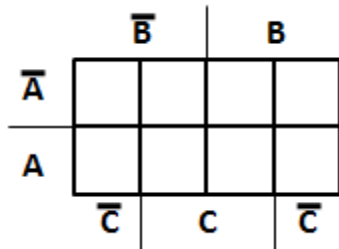


7. Minimize as expressões abaixo usando para tal o diagrama de Veitch-Karnaugh:

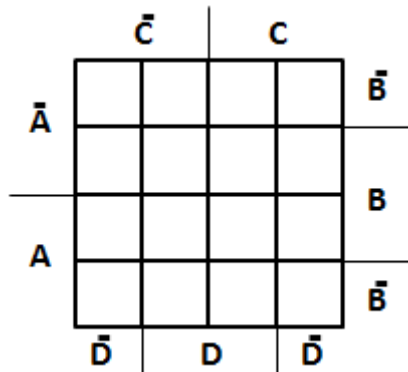
a) $\overline{A}\overline{B}\overline{C}\overline{D} + \overline{A}\overline{B}C\overline{D} + \overline{A}B\overline{C}\overline{D} + \overline{A}BC\overline{D} + \overline{A}\overline{B}\overline{C}D$



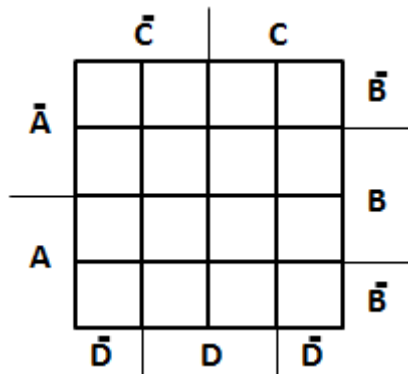
b) $(\overline{A}BC) + (\overline{A}B\overline{C}) + (\overline{A}B\overline{C}) + (ABC) + (A\overline{B}C)$



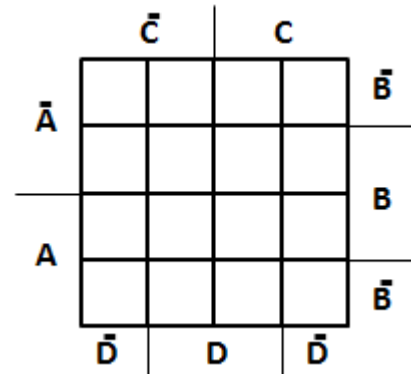
c) $\overline{A}\overline{B}\overline{C}\overline{D} + (\overline{A}\overline{B}\overline{C}D) + (\overline{A}\overline{B}C\overline{D}) + (\overline{A}\overline{B}CD)$



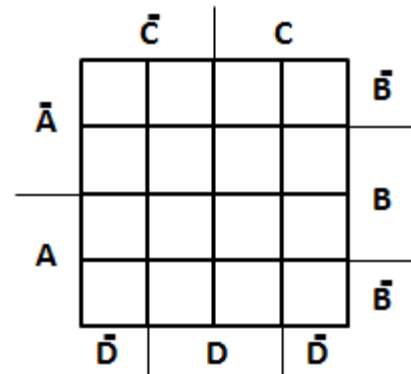
d) $(\overline{A}\overline{B}\overline{C}\overline{D}) + (\overline{A}\overline{B}C\overline{D})$



e) $\overline{A}BC\overline{D}\overline{E} + \overline{A}\overline{B}\overline{C} + \overline{D}E + \overline{C}D + \overline{A}D + \overline{A}\overline{B}$



f) $\overline{A}\overline{B}\overline{C}D + \overline{C}D + \overline{A}\overline{B}$



E tem gente que diz que matrizes só servem mesmo para ser tema de filmes!!!!!!